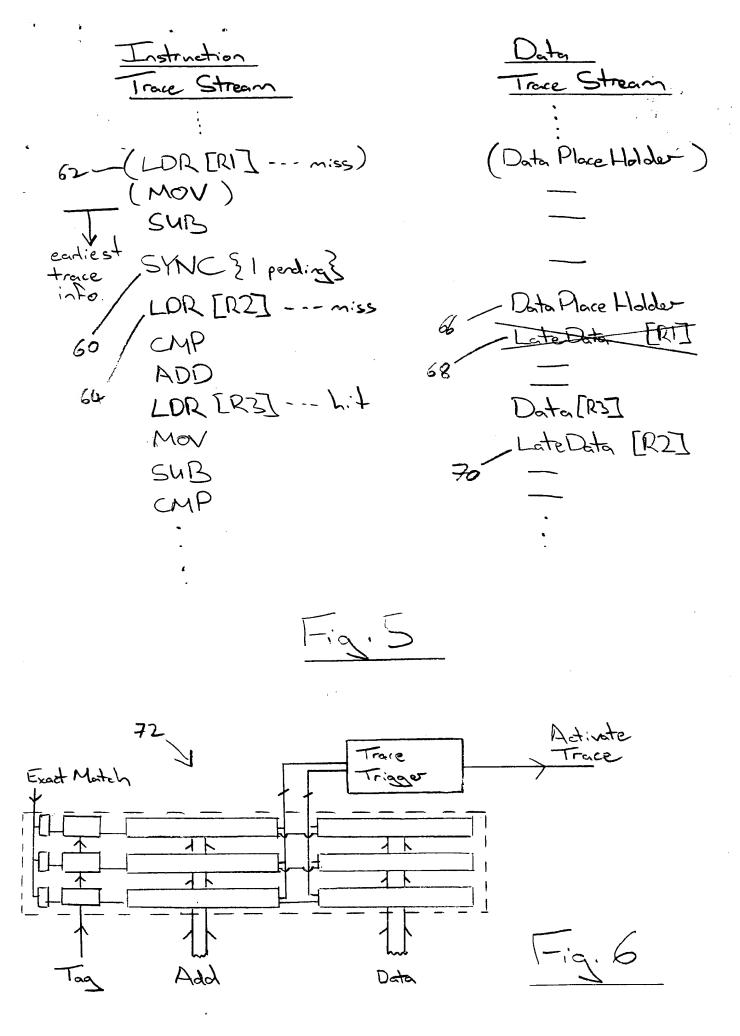


HI+/Miss Data Inst Data [Rm] 4:1 NA N/A Miss N/A N/A Miss Miss AIN 28 - Data [R2] CMP ANA Data [RI] N/A CICA

Fig. 2

MOVCMP 36 Data [R2]

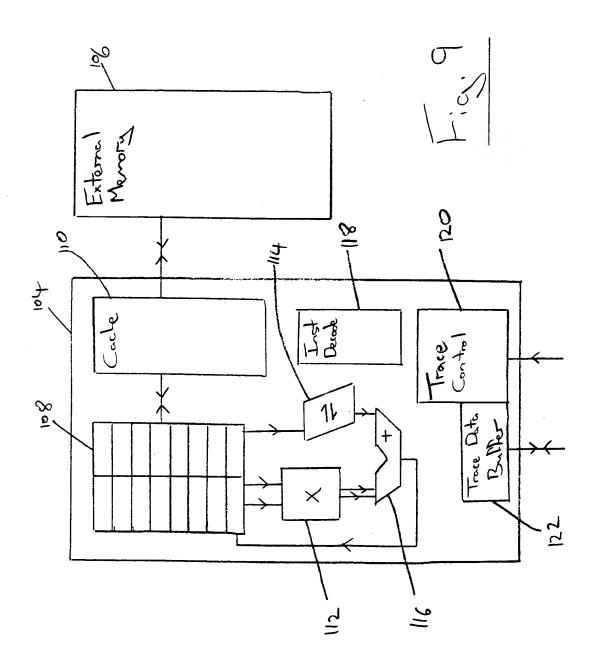
Data Place Hobber Tag 2 LDRIR] -- L:+ LDRIRZT -- miss 38 ADD 42 - Late Data Tag 2 [R3] 44 - Late Data Tag 1 [R1] MON SUB tiq. S DR[R]--miss CMP ADO Deta [R2] LOR[R]-- h:+ 52 Data Place Holder 1 perding 54 Late Data [RI] -LDR[R3]- · miss SUR SX - Data Place Lbbler 1 perding LDR[R4]-- miss Late Oata [R3] Late Oata [Ru]

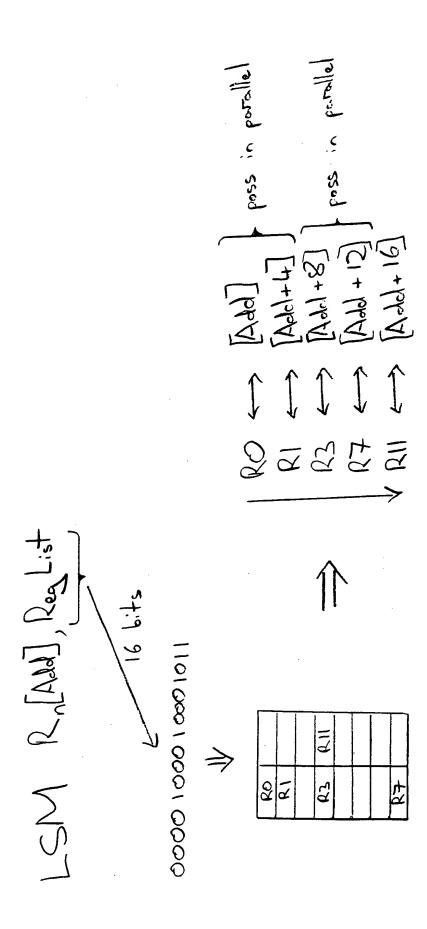


Instruction Stream - LDR [RI] --- miss Data [RI] - 1 Fig. 7 Exact Match Add Match Data Match Trace Advation Point

Fig. 8







- ioi |

; {LSM} , {DW3} , {Add+8}; {Place Holder}; {Place Holder}; Trace Octa Stream RII COUS [NOL) + 16] R7 (MA) + [NA)+12] > R3 (Mad +8]! [4+198+4] RO (M. [AN]

Court back to R3

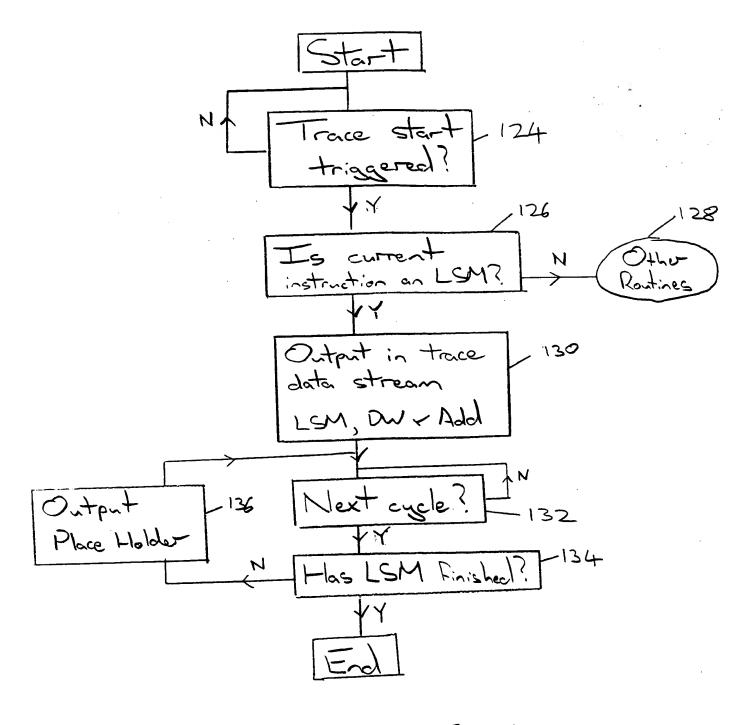


Fig. 12

trace stream 40 of register transfers Examine LSM instruction and count back to Find register of first traced 144

I-ig. 13

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